



**GES's R. H. Sapat College of Engineering, Management Studies and
Research, Nashik**
Department of Electronics and Telecommunication Engineering
IEEE Student Branch (STB60228643) & ESTA
A.Y. 2025-26



Report on two day Workshop on “Circuit to PCB: Professional Board Designing”

Date: 16th & 17th March 2026

Venue: VLSI & EDM Lab

The Department of Electronics and Telecommunication Engineering, in collaboration with ESTA and IEEE Student Branch, organized a two-day workshop for **Third Year E&TC students** on **“Circuit to PCB: Professional Board Designing”**, conducted by **Mr. D. M. Raut** and **Dr. K. V. Karad**, Assistant Professors, Department of Electronics and Telecommunication Engineering, **GES's R. H. Sapat College of Engineering, Management Studies and Research, Nashik**. The objective of this workshop was to provide students with practical knowledge of the PCB design process, circuit layout techniques, and fabrication methods used in industry, and to bridge the gap between theoretical concepts and real-time hardware implementation through hands-on training on PCB designing software and tools.



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